

Attorney Docket No.: 2102397-911400

**CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as FIRST CLASS MAIL in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on October 10, 2001

Kathleen Harrell  
(Name)

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Chih Hsin Wang et al.

Serial No: 09/916,555

Group Art Unit: 2812

Filed: July 26, 2001

Examiner: Not assigned

Title: Self Aligned Method Of Forming A Semiconductor Memory Array Of Floating Gate Memory Cells With Low Resistance Source Regions And High Source Coupling, And A Memory Array Made Thereby

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**RESPONSE TO NOTICE TO FILE MISSING PARTS  
OF UTILITY PATENT APPLICATION**

Box MISSING PARTS  
Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

In response to the "Notice to File Missing Parts of Utility Patent Application" mailed by the United States Patent and Trademark Office on September 10, 2001. The following documents are enclosed to complete the filing of the above-referenced patent application:


1. Copy of Notice to File Missing Parts
2. Executed Declaration
3. Power of Attorney by Assignee w/ courtesy copy of assignment
4. Fee transmittal (in duplicate)
5. Check in the amount of \$130.00 (missing parts surcharge)
6. Return Postcard

The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment to Deposit Account No. 07-1896.

Respectfully submitted,

GRAY CARY WARE & FREIDENRICH LLP

Dated: Oct 10, 2001

By   
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